

Intel Docket No: P15914 (2112-021)



**Expedited Procedures
Requested**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of : Assignee: Intel Corporation
BURTON, E. et al. : Confirmation No. 7414
Serial No. 10/607,550 : Examiner: Tran, L.
Filed: June 27, 2003 : Group Art Unit: 2818
For: ADDITION OF METAL LAYERS WITH SIGNAL REALLOCATION TO A
MICROPROCESSOR FOR INCREASED FREQUENCY AND LOWER POWER

*Enter w/ PCE
dated
4-25-06*

*C. Stealy
5-16-06*

AMENDMENT UNDER 37 CFR § 1.116

U.S. Patent and Trademark Office
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Alexandria, Virginia 22314

March 16, 2006

Sir:

The following amendments and remarks are submitted in response to the Final Office Action mailed on February 6, 2006, in connection with the above-identified application.

Amendments to the Claims are in the Listing of Claims beginning on page 2.

Remarks begin on page 6.